

Fig. 1

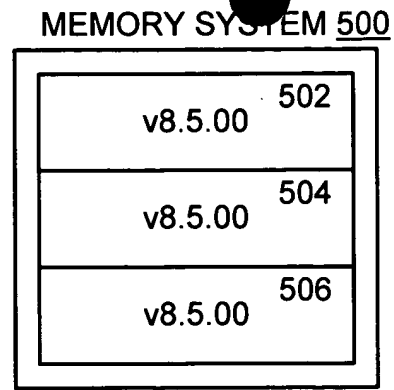
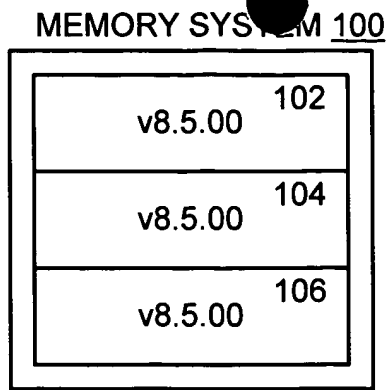


Fig. 2a

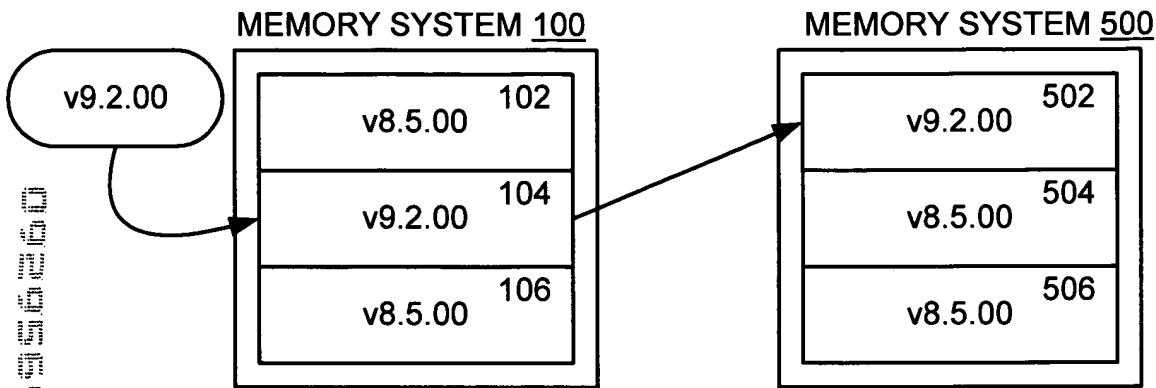


Fig. 2b

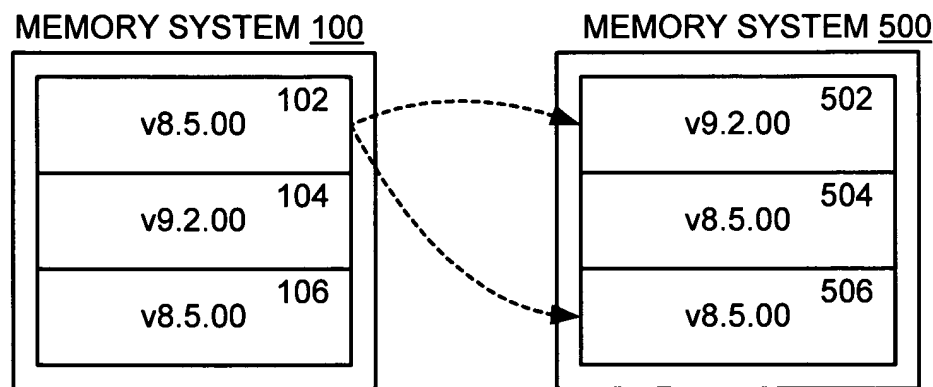


Fig. 2c

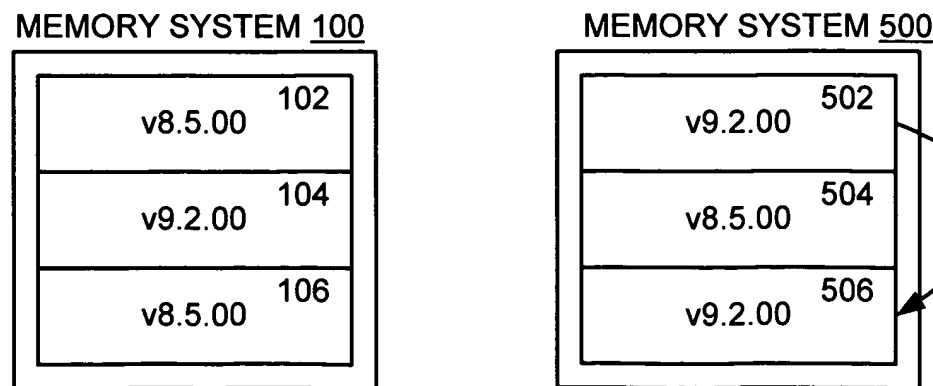


Fig. 2d

Patent 0695660

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graph TD
    START([START]) --> 600[LOAD ACTIVE ROM AND STANDBY RAM WITH NEW REVISION]
    600 --> 602[UPDATE STANDBY RAM AND STANDBY BRAM FROM ACTIVE RAM]
    602 --> 604[PASS CONTROL FROM ACTIVE CONTROL CARD TO STANDBY CONTROL CARD]
    604 --> 606[REWRITE ACTIVE BRAM WITH UPGRADED AND UPDATED VERSION FROM ACTIVE RAM]
    606 --> END([END])

```


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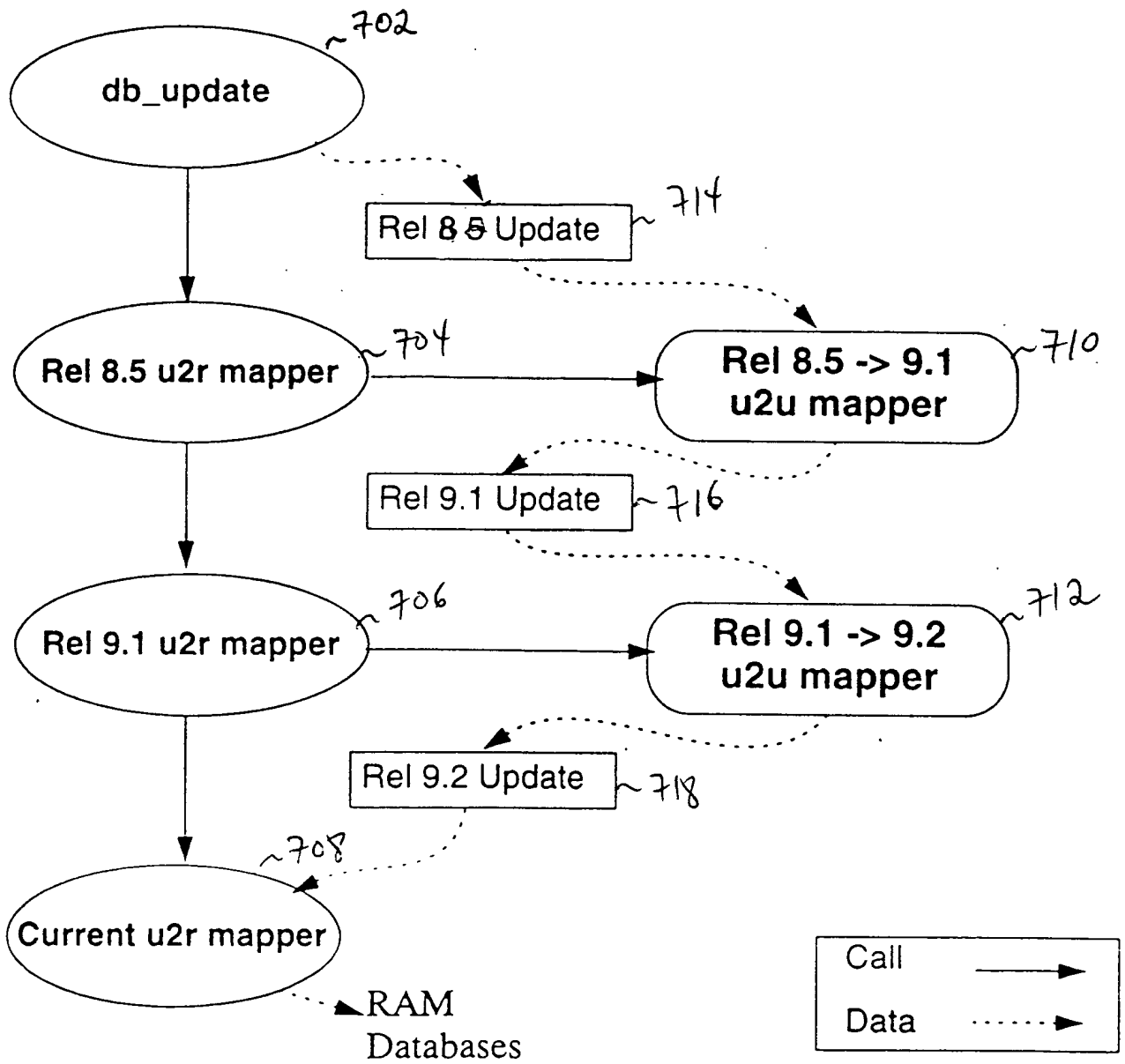


FIG. 4

MEMORY SYSTEM 100

v9.1.05	102
v9.1.05	104
v9.1.05	106

Fig. 5a

MEMORY SYSTEM 100

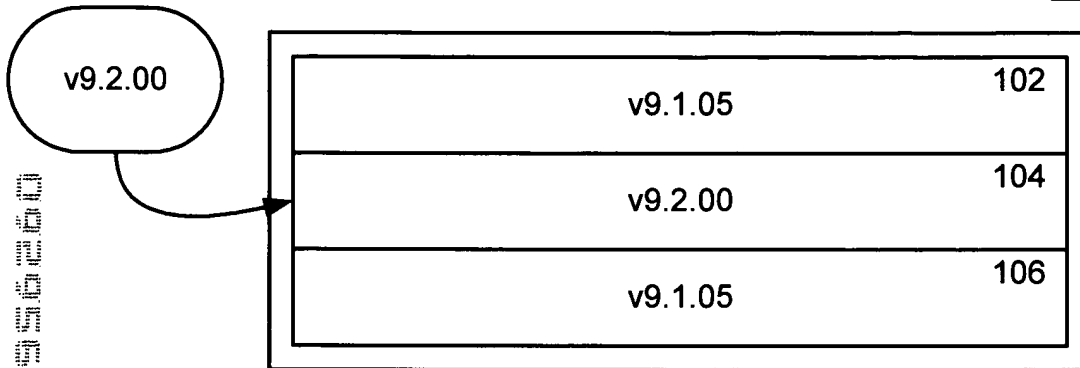


Fig. 5b

MEMORY SYSTEM 100

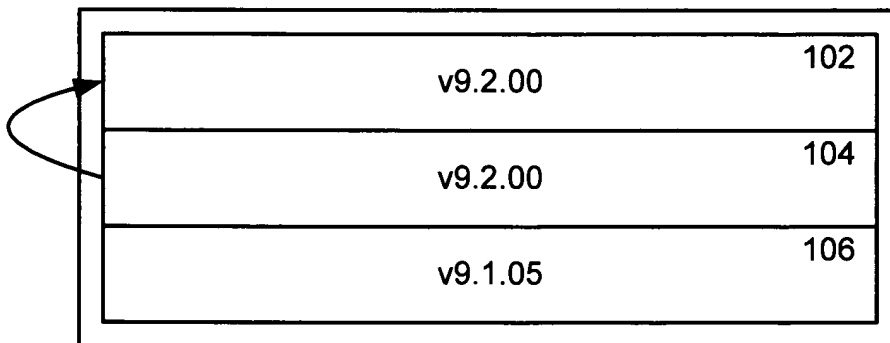


Fig. 5c

MEMORY SYSTEM 100

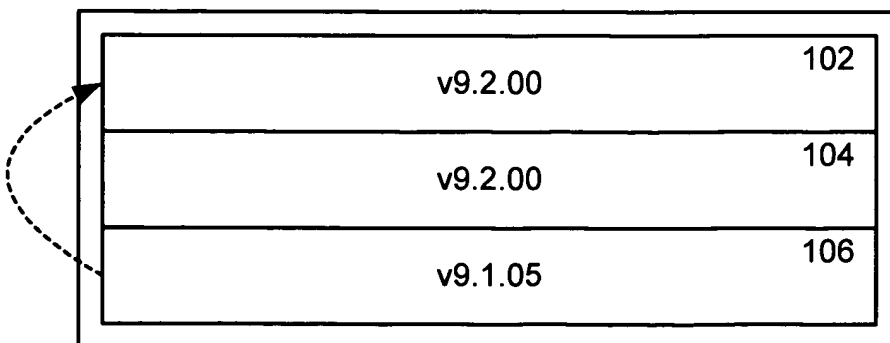


Fig. 5d

MEMORY SYSTEM 100

v9.2.00	102
v9.2.00	104
v9.2.00	106

Fig. 5e

Table 1. Mean values of the variables measured during the 60-min test

Method 600

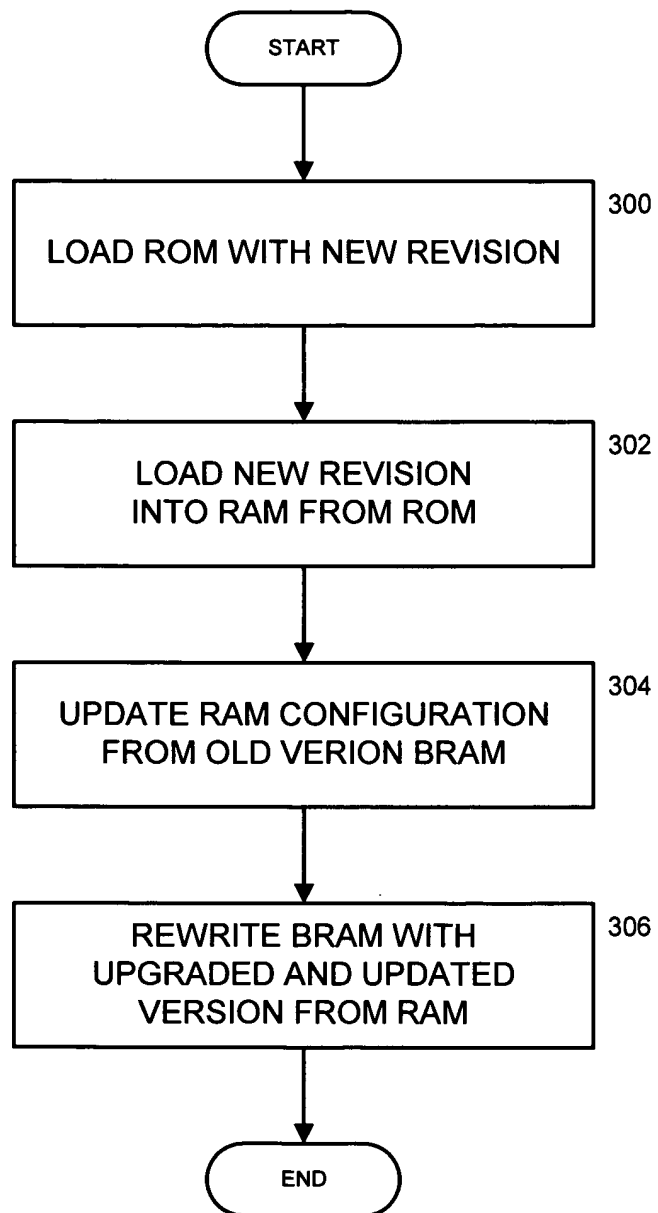


Fig. 6

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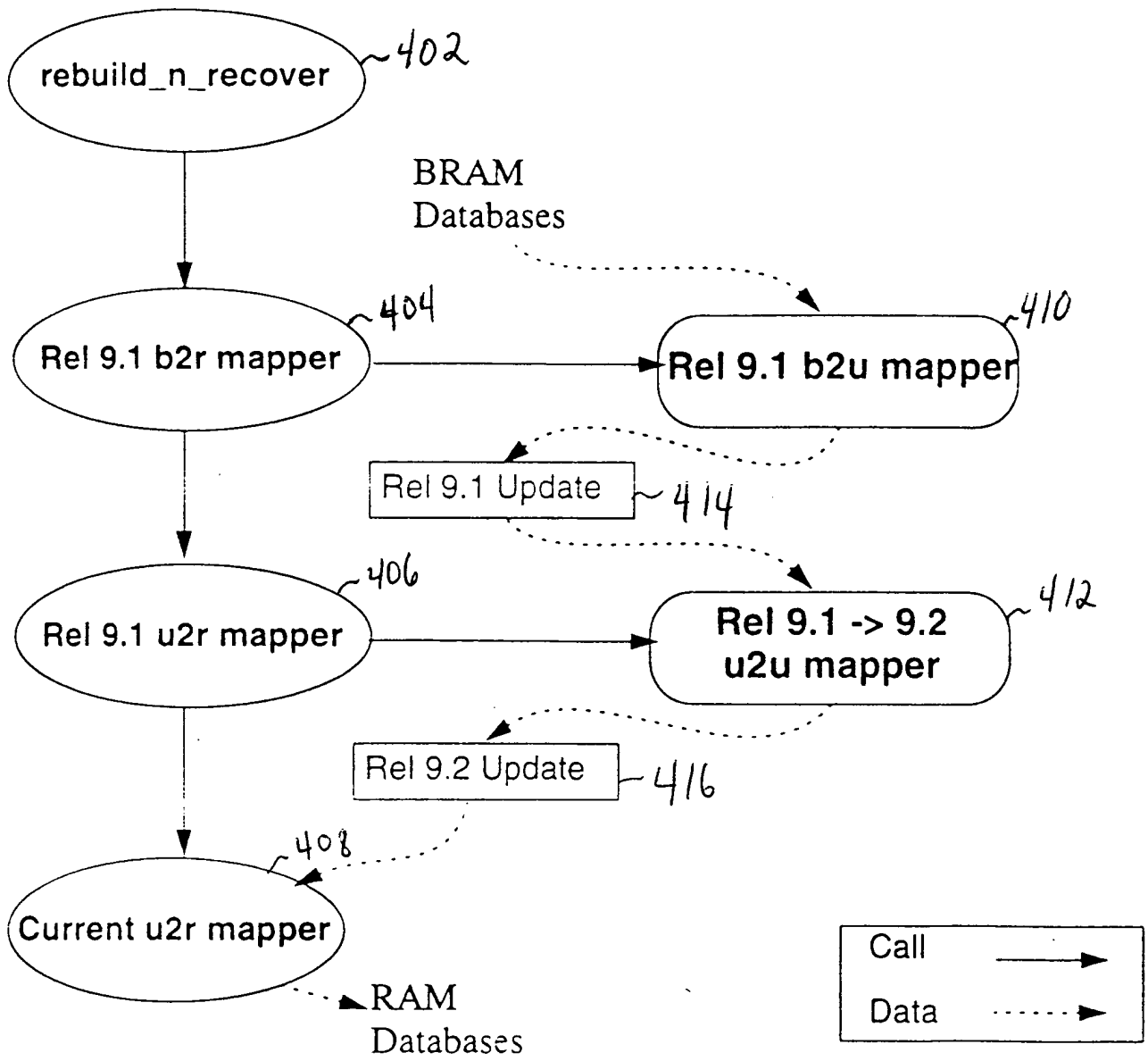


FIG. 7